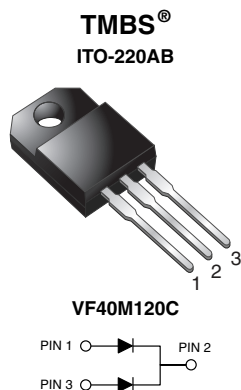


Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.46\text{ V}$ at $I_F = 5\text{ A}$



FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder bath temperature 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 20 A
V_{RRM}	120 V
I_{FSM}	250 A
V_F at $I_F = 20\text{ A}$	0.64 V
T_J max.	150 °C
Package	ITO-220AB
Circuit configuration	Common cathode

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102
M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25\text{ °C}$ unless otherwise noted)

PARAMETER	SYMBOL	VF40M120C	UNIT
Maximum repetitive peak reverse voltage	V_{RRM}	120	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	40 20	A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I_{FSM}	250	
Voltage rating of change (rated V_R)	dV/dt	10 000	V/ μ s
Isolation voltage from thermal to heatsink $t = 1\text{ min}$	V_{AC}	1500	V
Operating junction and storage temperature range	T_J, T_{STG}	-40 to +150	°C

ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)					
PARAMETER	TEST CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	$I_F = 5\text{ A}$	$V_F^{(1)}$	0.54	-	V
	$I_F = 10\text{ A}$		0.64	-	
	$I_F = 20\text{ A}$		0.79	0.89	
	$I_F = 5\text{ A}$	$T_A = 125\text{ }^{\circ}\text{C}$	0.46	-	
	$I_F = 10\text{ A}$		0.54	-	
	$I_F = 20\text{ A}$		0.64	0.72	
Reverse current per diode	$V_R = 90\text{ V}$	$T_A = 25\text{ }^{\circ}\text{C}$	4	-	μA
		$T_A = 125\text{ }^{\circ}\text{C}$	3	-	mA
	$V_R = 120\text{ V}$	$T_A = 25\text{ }^{\circ}\text{C}$	-	500	μA
		$T_A = 125\text{ }^{\circ}\text{C}$	6	32	mA

Notes

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width $\leq 20\text{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	VF40M120C	UNIT
Typical thermal resistance per diode	$R_{\theta JC}$	4.0	$^{\circ}\text{C/W}$

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
ITO-220AB	VF40M120C-M3/4W	1.76	4W	50/tube	Tube

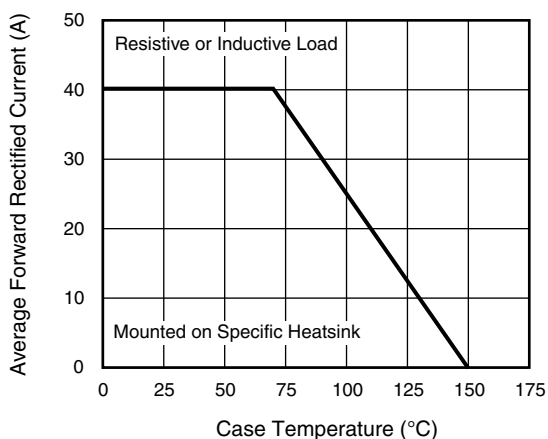
RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)


Fig. 1 - Maximum Forward Current Derating Curve

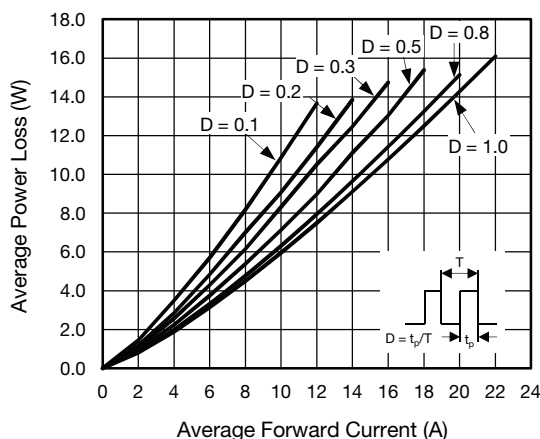


Fig. 2 - Forward Power Loss Characteristics Per Diode

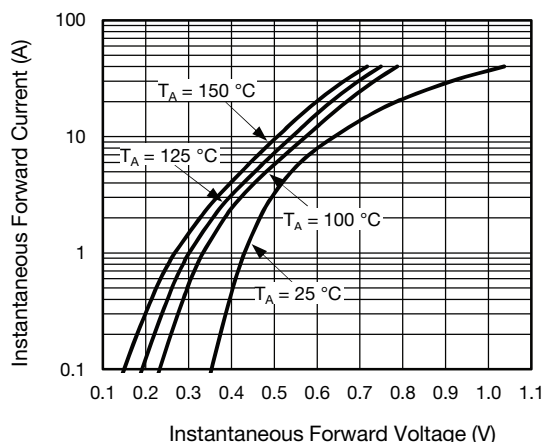


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

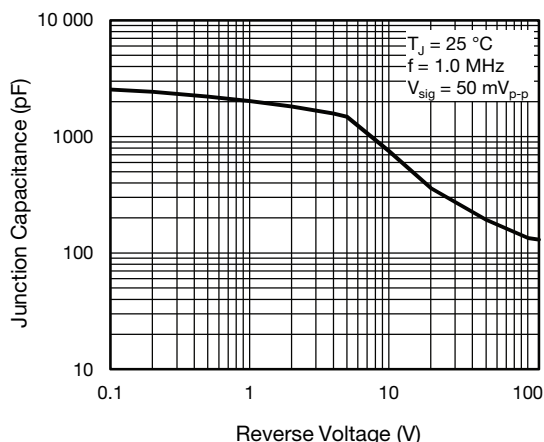


Fig. 5 - Typical Junction Capacitance Per Diode

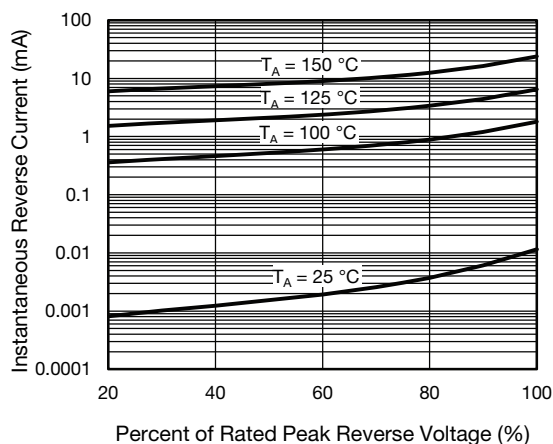


Fig. 4 - Typical Reverse Characteristics Per Diode

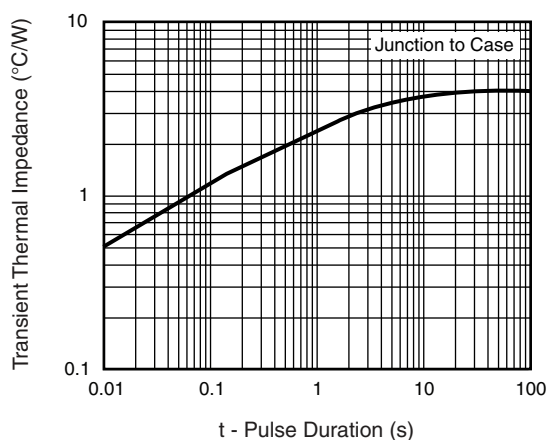
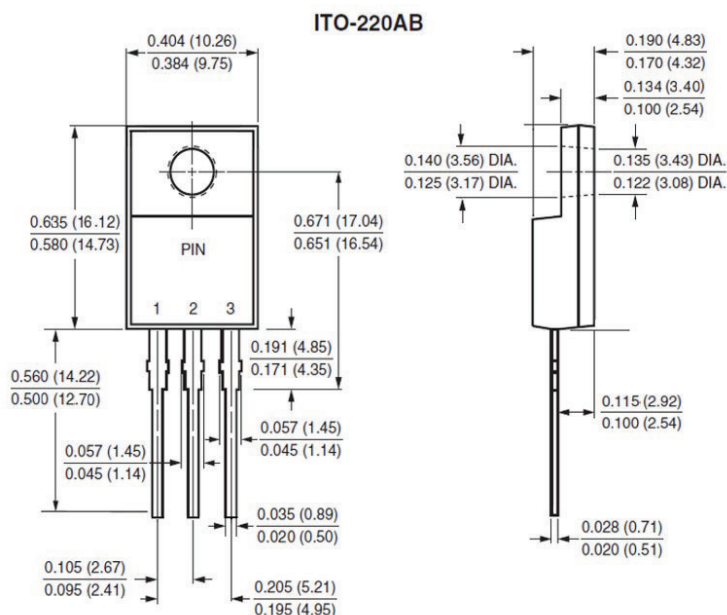


Fig. 6 - Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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