

2A SURFACE-MOUNT STANDARD RECOVERY BRIDGE RECTIFIER

Product Summary (@T_A = +25°C)

V _{RRM} (V)	I _O (A)	V _F (V)	I _R (μA)
1000	2	0.95	5

Description and Applications

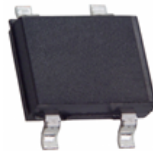
Suitable for AC to DC bridge full wave rectification for SMPS, LED lighting, adapters, battery chargers, home appliances, office equipment, and telecommunication applications.

Features and Benefits

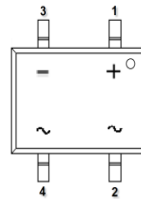
- Glass Passivated Die Construction
- Miniature Package Saves Space on PC Boards
- Low Leakage Current
- Ideal for SMT Manufacturing
- Low-Forward Voltage Drop
- UL Listed Under Recognized Component Index, File Number E364304
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

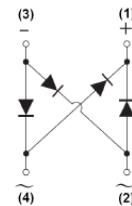
- Package: HDS
- Package Material: Molded Plastic.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (E3)
- Polarity: As Marked on Body
- Weight: 0.0923 grams (Approximate)



Top View



Pin Diagram



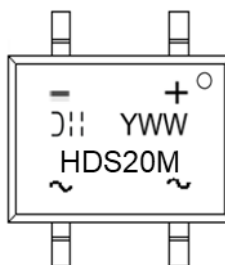
Internal Schematic

Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
HDS20M-13	HDS	5,000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



- HDS20M = Product Type Marking Code
 YWW = Manufacturers' Code Marking
 YWW = Date Code Marking
 Y = Last Digit of Year (ex: 5 = 2025)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	1000	V
RMS Reverse Voltage	V _{R(RMS)}	700	V
Average Rectified Output Current (Note 5) @ T _C = +88°C	I _O	2.0	A
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	55	A
Non-Repetitive Peak Forward Surge Current, 1ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	110	A
I ² t Rating for Fusing (1ms < t < 8.3ms)	I ² t	8.03	A ² S

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Ambient (Note 6) (Per Element)	R _{θJA}	20	°C/W
Typical Thermal Resistance, Junction to Case (Per Element)	R _{θJC}	16	°C/W
Typical Thermal Resistance, Junction to Lead (Per Element)	R _{θJL}	18	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	1,000	—	—	V	I _R = 5μA
Forward Voltage (Per Element)	V _F	—	0.92	0.95	V	I _F = 1A, T _A = +25°C
Leakage Current (Note 7) (Per Element)	I _R	—	0.11 45	5 100	μA	V _R = 1,000V, T _A = +25°C V _R = 1,000V, T _A = +125°C
Total Capacitance (Per Element)	C _T	—	13	—	pF	V _R = 4V, f = 1.0MHz

- Notes:
5. Perform static test after the temperature of oven is steady 20 minutes.
 6. Device mounted on glass epoxy substrate with 1oz/ft², 30mmx30mm copper pad per pin.
 7. Short duration pulse test used to minimize self-heating effect.

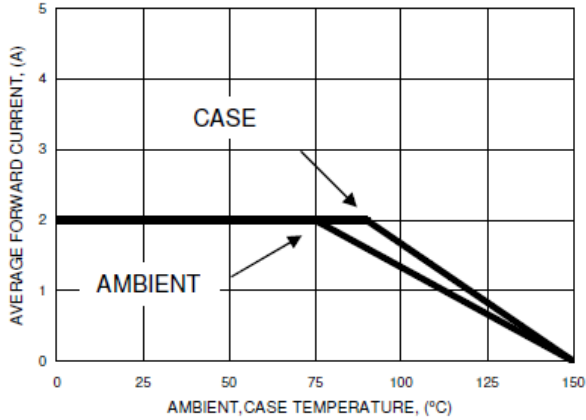


FIG.1- FORWARD CURRENT DERATING CURVE

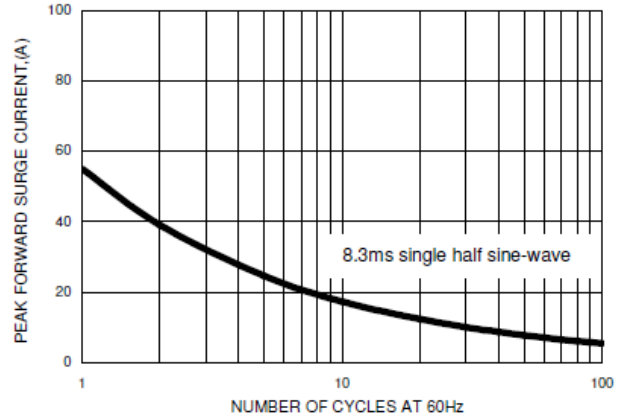


FIG.2- MAXIMUM NON-REPETITIVE SURGE CURRENT

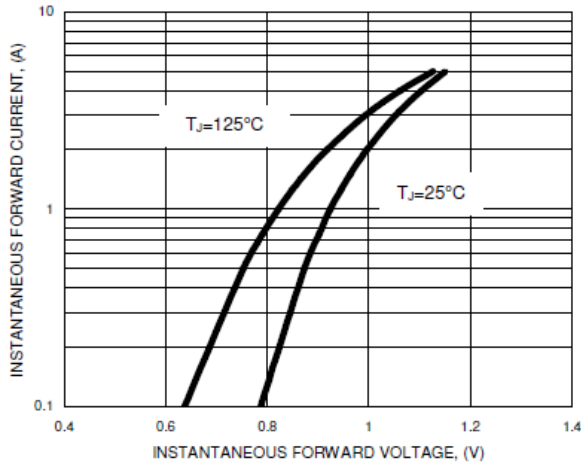


FIG.3- TYPICAL FORWARD CHARACTERISTICS

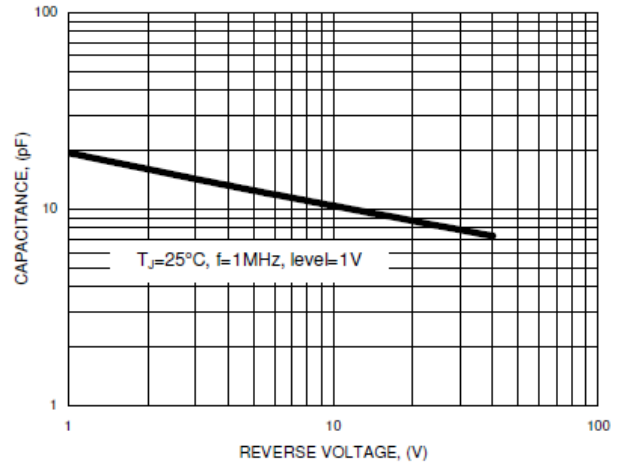


FIG.4- TYPICAL JUNCTION CAPACITANCE

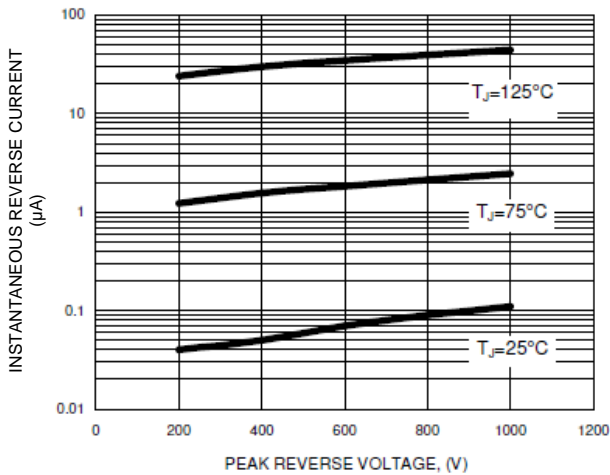
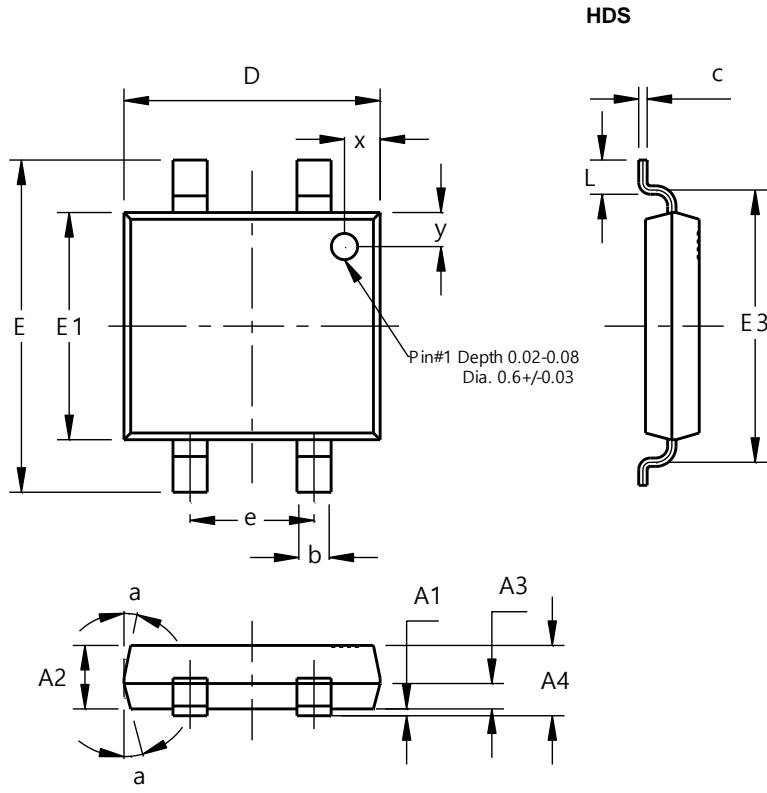


FIG.5- TYPICAL REVERSE CHARACTERISTICS

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

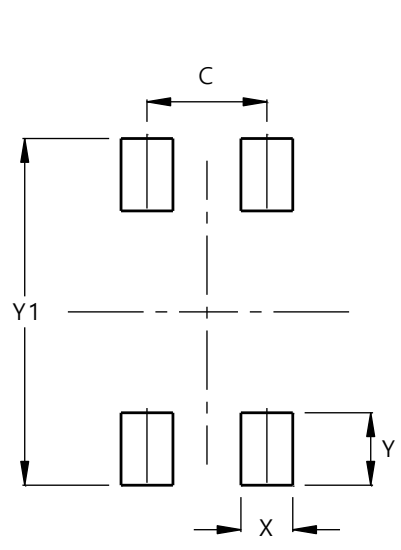


HDS

HDS			
Dim	Min	Max	Typ
A1	0.00	0.15	--
A2	1.20	1.30	--
A3	0.43	0.63	--
A4	1.20	1.40	--
b	0.45	0.75	--
c	0.10	0.30	--
D	4.85	5.25	--
E	6.40	6.80	--
E1	4.25	4.65	--
E3	5.20	5.60	--
e	--	--	2.54
L	0.40	0.80	--
x	0.45	0.85	--
y	0.45	0.85	--
a	--	--	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



HDS

Dimensions	Value (in mm)
C	2.54
X	1.00
Y	1.50
Y1	7.10

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